

IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) A circuit ~~(2)~~ for generating a composite output picture signal ~~(6)~~ such as a picture signal ~~(36)~~ comprising a PIP (Picture-in-Picture) signal ~~(38)~~, or a DW (Double Window) signal, on the basis of at least two input picture signals ~~(4.1, 4.2)~~, the circuit comprising:

color decoding means ~~(8)~~ for decoding the at least two input picture signals ~~(4.1, 4.2)~~, the color decoding means ~~(8)~~ comprising combining means ~~(14)~~ for losslessly combining the at least two input picture signals into one combined picture signal ~~(16)~~, and one color decoder ~~(18)~~ for decoding the one combined picture signal ~~(16)~~ to ~~obtain form~~ a single decoded combined picture signal ~~(12)~~; and

composing means ~~(10)~~ for generating the composite output picture signal ~~(6)~~ on the basis of the single decoded combined picture signal ~~(12)~~.

2. (Currently Amended) A The circuit according to as claimed in claim 1, wherein the combining means ~~(14)~~ ~~comprise~~ comprises a multiplexer for time-multiplexing the at least two input picture signals ~~(4.1, 4.2)~~ ~~so as to obtain to form~~ the one combined picture signal ~~(16)~~.

3. (Currently Amended) ~~A~~ The circuit according to ~~as~~ claimed in claim 2, wherein the multiplexer comprises a buffer-memory.

4. (Currently Amended) ~~A~~ The circuit according to ~~as~~ claimed in claim 1, wherein the composing means ~~(10)~~ comprise ~~comprises~~ a de-multiplexer ~~(26)~~ for the time de-multiplexing of the decoded combined picture signal ~~(12)~~ into at least two decoded picture signals ~~(30.1, 30.2)~~ for further processing ~~(28)~~ to obtain from the composite output picture signal ~~(6)~~.

5. (Currently Amended) ~~A~~ The circuit according to ~~as~~ claimed in claim 4, wherein the composing means ~~(10)~~ further comprise comprises a memory ~~(24)~~ and a micro-processor ~~(28)~~ for the further processing of the at least two decoded picture signals ~~(30.1, 30.2)~~.

6. (Currently Amended) A television apparatus ~~provided with~~ comprising:

a ~~the~~ circuit ~~(2)~~ according to ~~as~~ claimed in claim 1, and

a display device ~~(D)~~ for displaying the composite output

5 picture signal ~~(6)~~.

7. (Currently Amended) A video tape recorder ~~provided with~~
comprising:

_____a circuit ~~according to~~as claimed in claim 1.